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**Fax**

Page 1 of 8

**Urgent – Deliver directly to examiner**

**Confidential**

**Date:** June 22, 2005

**To:**  
Examiner: H. Day  
USPTO

**Fax:**  
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**Art Unit:**  
2128

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**M/S:**  
LF3

**Subject:** Methods and Apparatus for Modeling and Circuits with Asynchronous Behavior

**Application No.:** 09/531,910 **Inventor:** Silaram YADAVALLI, et al.

**Filed:** 03/20/2000

**Docket No.:** P7896

**A CONFIRMATION COPY OF THIS DOCUMENT:**  
**WILL NOT BE SENT**

**Date:** June 22, 2005

**Included in this transmission:**  
Fax Cover Sheet (1 page)  
Supplemental Amendment (7 pages)

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**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re patent application of:

Sitaram YADAVALLI, et al.

Serial No.: 09/531,910

Group Art Unit 2128

Filed: March 20, 2000

Examiner: H. Day

FOR: METHOD AND APPARATUS FOR MODELING AND  
CIRCUITS WITH ASYNCHRONOUS BEHAVIOR

**SUPPLEMENTAL AMENDMENT**

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Supplemental to the response filed March 3, 2005, please amend the above-identified application as follows:

P7896